

Application No.	Applicant(s)	
10/628,916	POTLAPALLI ET AL.	
Examiner	Art Unit	
Peguy JeanPierre	2819	

SEARCHED				
Class	Subclass	Date	Examiner	
341	155			
	141			
	162			
	160			
710	65			
	69	5/17/2004	PJP	

INTERFERENCE SEARCHED				
Class	Subclass	Date	Examiner	
341	155	5/17/2004	PJP	
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	.I.			
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SEARCH NOTES (INCLUDING SEARCH STRATEGY)				
	DATE	EXMR		
EAST				
logic circuit is coupled to ADC to select either fixed, variable or set of analog input signals to be converted	5/17/2004	PJP		
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